

# RESUME



GIRISH CHANDRA GHIVELA

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At-Dharabgarh, Po-Goilbhadi, Dist-Bolangir, Odisha-767033

## OBJECTIVE

Seeking a position in an institute that will utilize my strong quantitative skills and ability to take on new challenges to full effect and offers professional growth while being resourceful, innovative. To secure a challenging position where I can effectively contribute my skills.

## WORK EXPERIENCE:

**A total of 4 years of teaching experience in educational institutions.**

- Worked as Guest Faculty in Electronics Engineering in Govt. College of Engineering, Kalahandi, Bhawanipatna from August ,2010 to June ,2012.
- Worked as a lecturer in Electronics Engineering (Contractual) in Govt. College of Engineering, Kalahandi, Bhawanipatna from August ,2014 to June ,2016.

## ACADEMIC QUALIFICATION:

QUALIFICATION	SPECIALIZATION	INSTITUTE	YEAR	CGPA
M.Tech	Communication Systems Engineering	Visvesvaraya National Institute of Technology, Nagpur	2014	8.25
B.Tech	Electronics and Tele-Communication Engineering	Biju Pattnaik University Of Technology, Rourkela	2010	7.62
CLASS XII(HSC)	Physics, Chem, Maths, Biology	D.A.V. College ,Titilagarh	2006	53.23%
CLASS X(SSC)	-----	Maheswar Hindi High School ,Titilagarh	2004	76.13%
GATE-Exam Paper:EC	2012 qualified with 98.2 percentile, AIR :3183, Total Candidates :176944, marks:32.67, REGD No.EC 6162253 2011 qualified with 95.5 percentile 2014 qualified with 91.15 percentile			

## PROJECTS/ACTIVITIES

- **Characterization and noise analysis of IMPATT diode using different Solid state materials at Ka-band, ( M.Tech)**

Synopsis: DC, AC and small signal noise Analysis of IMPATT Diode using different solid state materials like Si, Ge, GaAs, InP, WzGaN, 4H-SiC materials, as well as their comparative study of efficiency, noise measure, power handling capability so that it will be helpful for design of the IMPATT diode for different microwave applications.

- **Adaptive Digital Image Filter Using Artificial Neural Network” in MATLAB (B.tech)**

Synopsis: In this project various noise conditions are studied and efficient digital image filter based on artificial neural network is designed to suppress this noise efficiently.

- **Traffic Light Controller Using VHDL (B.tech)**

Synopsis: By this project I judge that it may cost less as compare to other method of implementation of traffic light controller.

### **PUBLICATIONS:**

**The details of published and communicated research Papers are.....**

1. J sengupta, **Girish Chandra Ghivela**, A V Gajbhiye, M Mitra, " Measurement of Noise and Efficiency of 4H-SiC IMPATT diode at Ka-band", **International Journal of Electronics Letter, Vol.4, issue-02,pp.134-140, 2016. (SCI Journal , Taylor and Francis Publication).**
2. J sengupta, **Girish Chandra Ghivela** ,M Mitra, " Dynamic Characterization and Noise Analysis of 4H-SiC Impatt Diode at Ka Band",**IJSCE,Vol-4,issue-1,pp.145-149,March-05,2014.**
3. **Girish Chandra Ghivela**, J. Sengupta, M Mitra, " Comparison of 4H-SiC IMPATT diode in Ka- and W-band", **communicated to International Journal of Electronics.**
4. J sengupta, **Girish Chandra Ghivela**, A V Gajbhiye, B Jothe, M Mitra, "Temperature dependence of 4H-SiC IMPATT Diode at Ka band", **IJECS,Vol-19,issue-2,April,2014.**
5. **Girish Chandra Ghivela** , J Sengupta, M Mitra, "Space Charge Effect of IMPATT diode based on Different Solid state Materials at Ka-Band " **communicated to Journal of Semiconductor and Technology.**

### **COMPUTER PROFICIENCY**

- **Programming Languages:** C, C++, basics of Java
- **Software Packages:** Mat lab, ADS and basics of Exata, CADFEKO,AWR, HFSS
- **Working on Windows**
- Certified Course on advance C and Core JAVA

### **Workshops/ Seminars:**

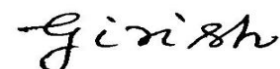
- Participated in the 2 week seminar on Topic entitled "RF, and Microwave Communication technology" by TEQUIP-II at VNIT Nagpur.
- Participated in the one day workshop on Virtual Laboratories and Instrumentation at VNIT, Nagpur held by IIT, Kharagpur.
- Participated in the One week seminar on Advanced Digital Signal Processing (STC) at VNIT Nagpur.

**SCHOLARSHIP: Recipient of GATE SCHOLARSHIP for two years provided by MHRD,GOI.**

### **AREA OF INTEREST:**

- Semiconductor photonics, Solid state devices, integrated photonics.

I hereby acknowledge that the information furnished above is correct to the best of my knowledge.



(Girish Chandra Ghivela)