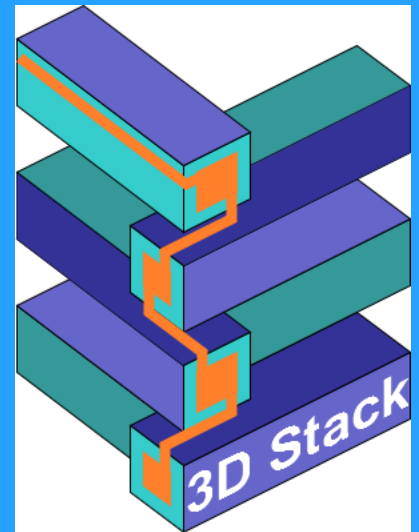


Nanoscale heat transport

Dr Alexandre Cuenat
Materials Division
Electrochemistry group

National Physical Laboratory, Teddington, UK



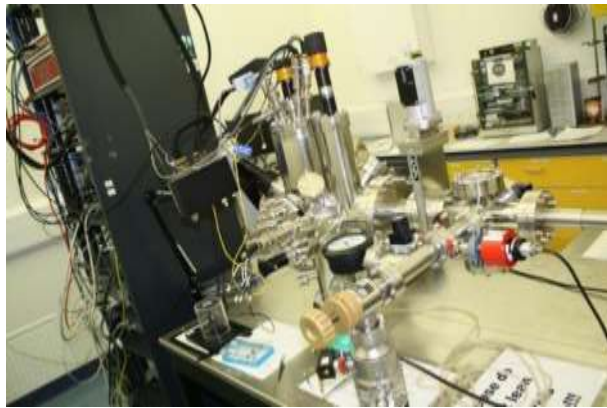
<http://empir.npl.co.uk/3dstack/>

Energy conversion efficiency in solid state materials across length scales

Accurate measurement of **heat flux** and energy **conversion efficiency** in solid state materials across length scales

Energy transport and dissipation in materials at the micro-nanoscale are key to

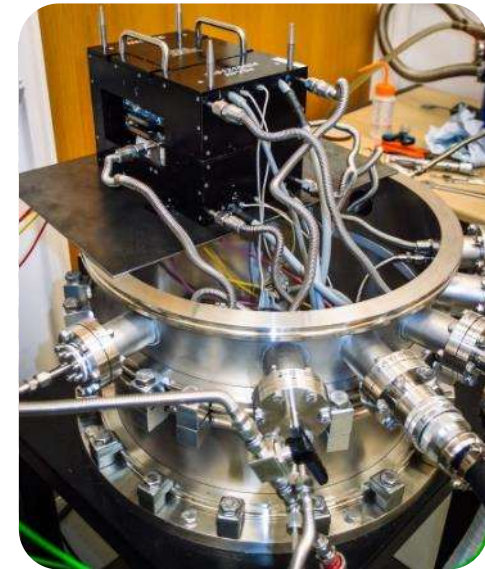
- ❑ **Direct Energy conversion:** thermoelectric, electrocaloric, photovoltaic,...
- ❑ **High current-density devices:** power electronics or emerging logic devices



SPM measurement of transport properties at the nanoscale

Cuenat et al, Nanotechnology 23 045703 (2012)

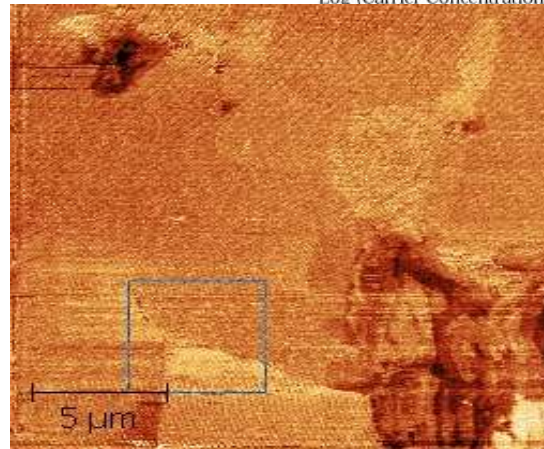
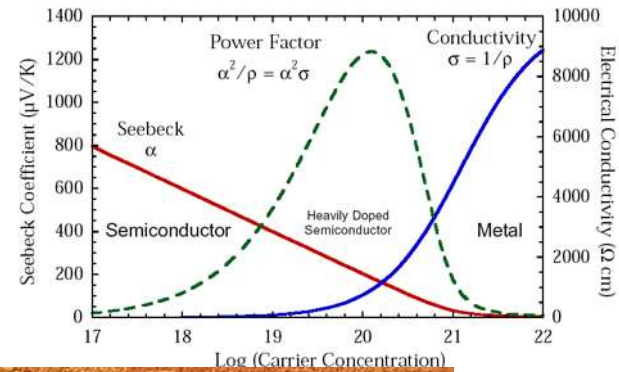
We are developing new traceable **nanoscale methods** to measure materials properties that link directly to **power conversion** in devices



Accurate characterisation of thermoelectric generators up to 900K

Performance improvement in TE

$$ZT = \frac{S^2 \sigma}{\kappa} T = \frac{\text{Power factor}}{\text{Thermal cond.}} T$$



Most of current improvement comes for thermal conductivity reduction

Heating in current-carrying nanostructures

The basic problem of thermoelectricity is the following:

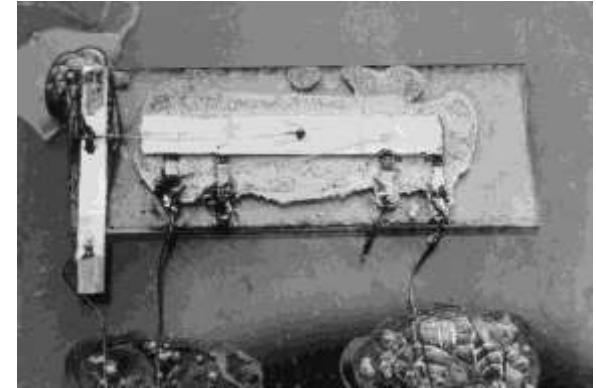
“Given a system consisting of one or several chemical phases and having a specified temperature distribution, it is required to determine the electronic electrochemical potential at any and all points within the system as well as at points in the immediate surroundings of the system.” Domenicali

RMP 1954

We are not alone to have this problem

A Brief History

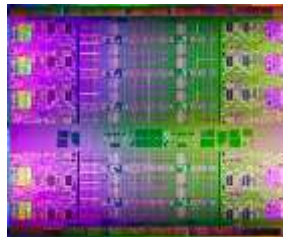
- **1958: First integrated circuit**
Flip-flop using two transistors
From Texas Instruments



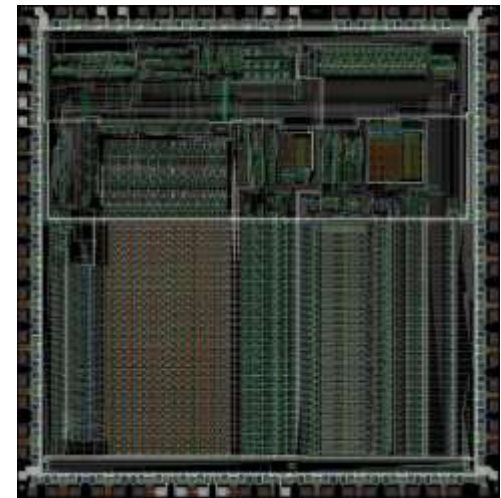
Source Texas Instruments

- **1985 ARM1**
25'000 transistors
3'000 nm process

- **2016 Intel 22 Core Xeon Broadwell**
7.2 billion transistors
14 nm process

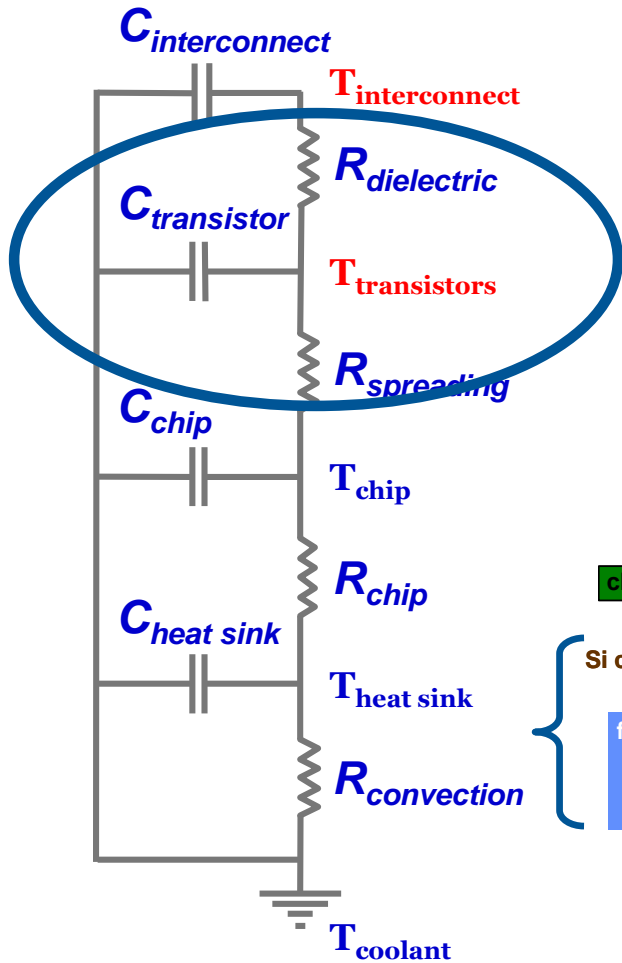


Source: Intel

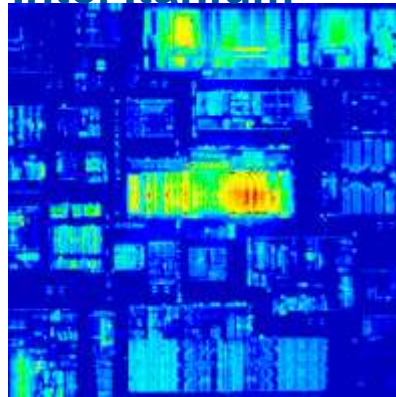


Source visual6502

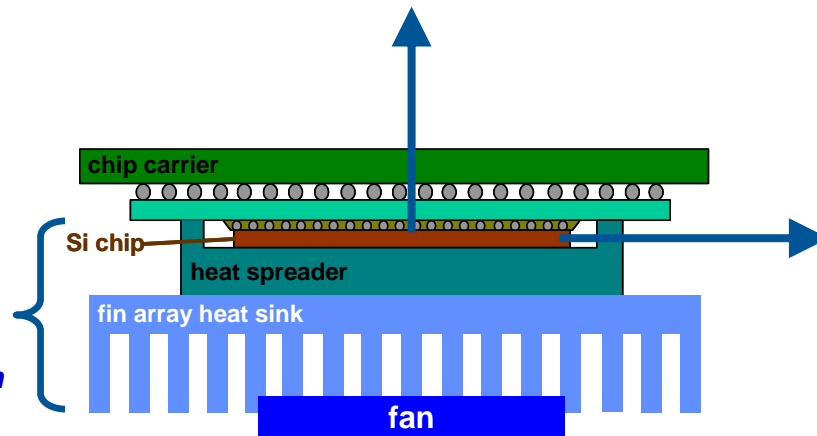
Where Does the Heat Come From?



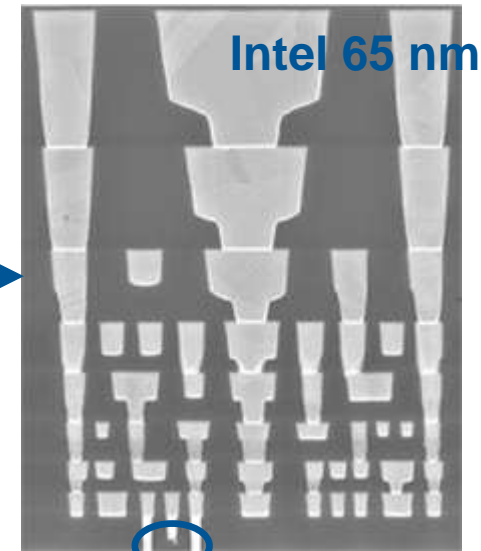
Intel Itanium



Top view
Hottest spots > 300 W/cm²



Cross-section
8 metal levels + ILD

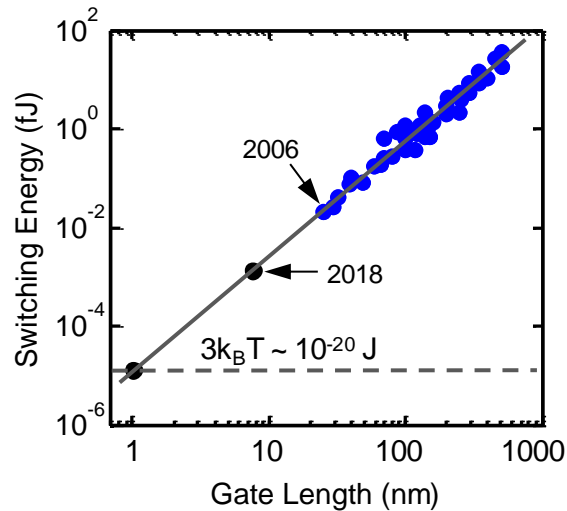


Intel 65 nm

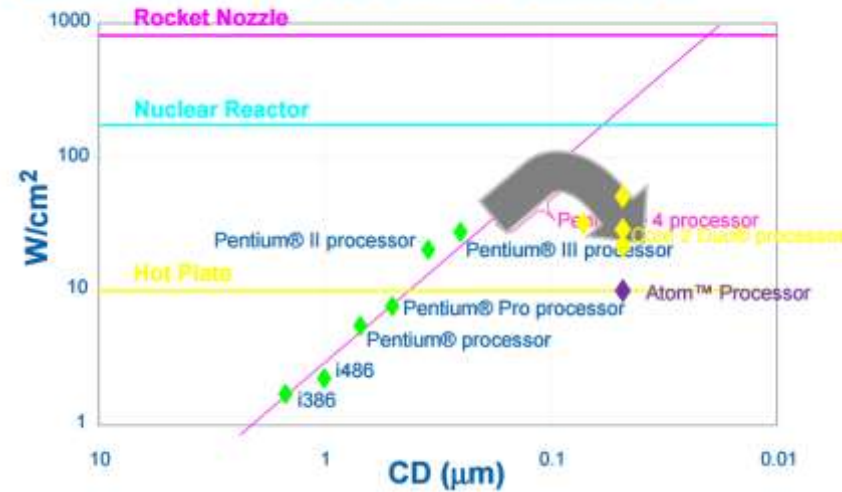
Transistor < 100 nm

Power Dissipation: Transistor → CPU

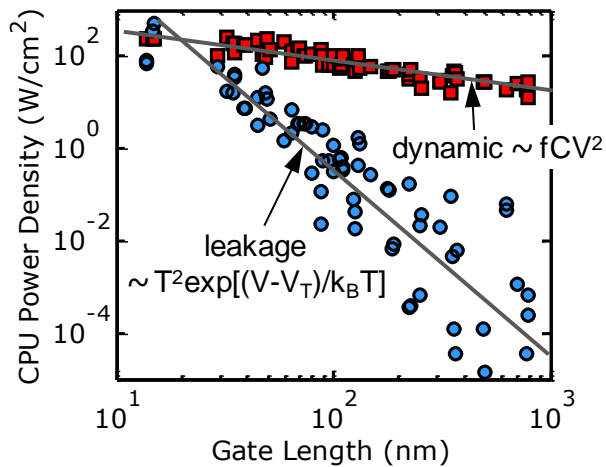
Single Transistor



Power Density vs. Critical Dimension

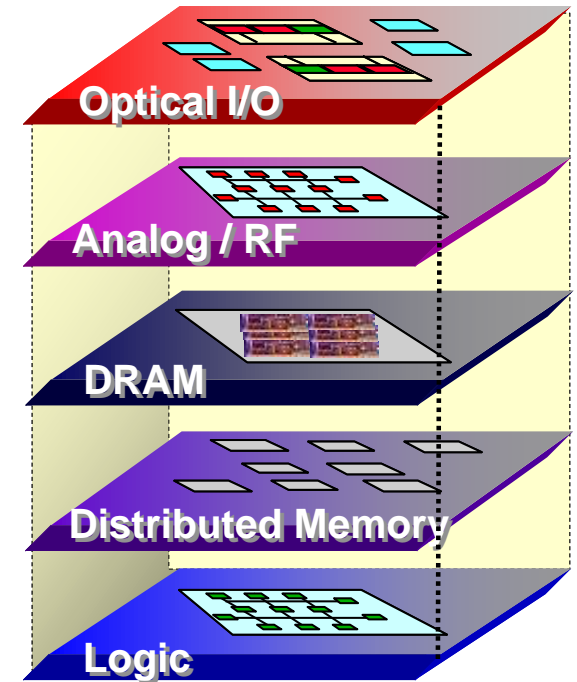
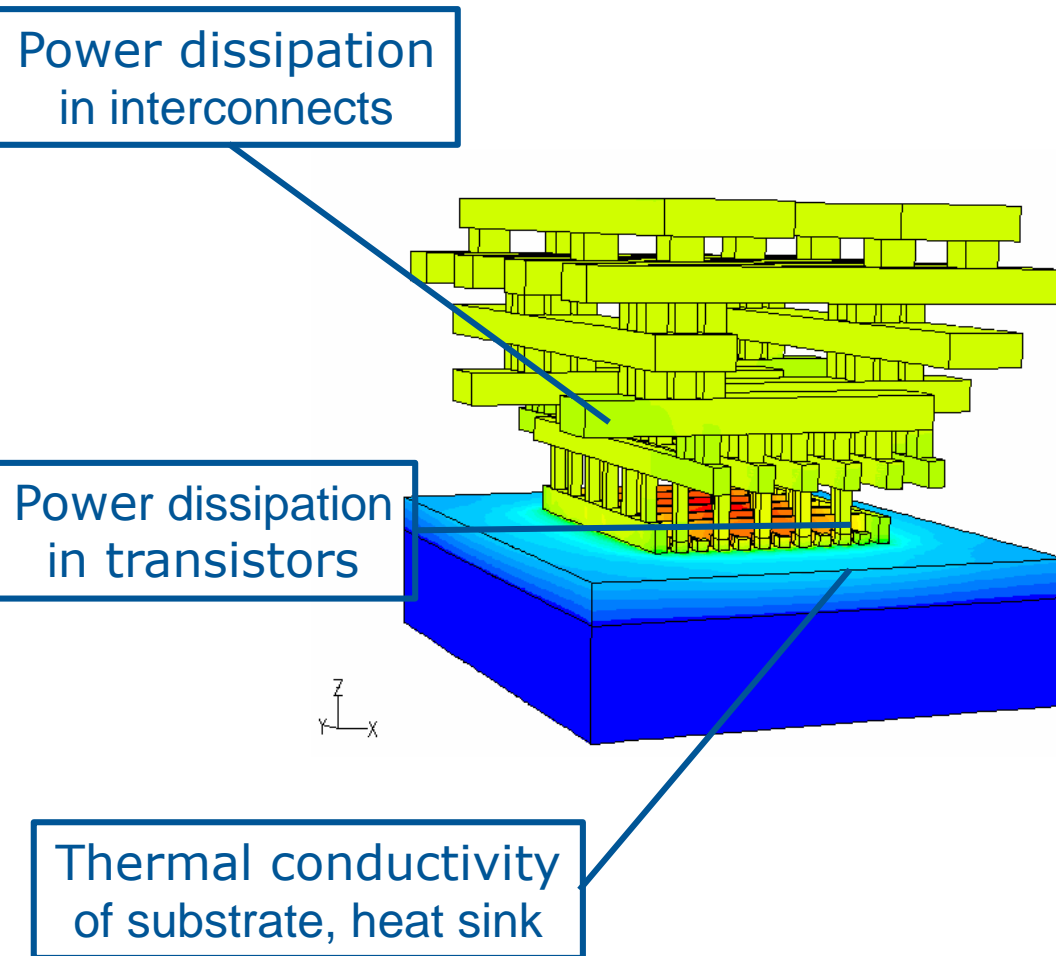


x 1,000,000,000 Transistors?



- E. Pop, *Nano Research* 3, 147 (2010)
 W. Haensch, *IBM J. Res. Dev.* 50, 339 (2006)
 R. Cavin, *J. Nanoparticle Res.* 8, 841 (2006)

More on Chip-Level Complexity



3-D integrated circuits = the ultimate density limit

How do we get the power in?
How do we take the heat out?

Metrology for manufacturing 3D stacked integrated circuits

EMPIR – Industry June 2015-May2018

3D-devices will combine logic, memories, imagers and MEMS from different wafers of various foundries using different manufacturing processes optimized at the right node.

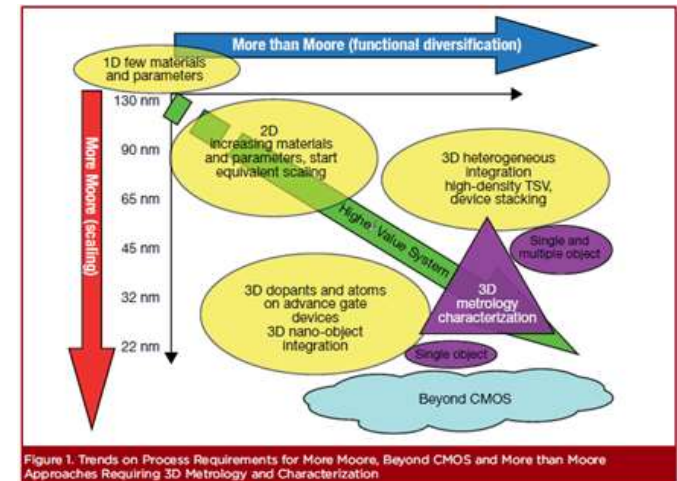
- ✓ **Traceability of the measurement and standardization will be mandatory**

Devices are “stacked” and connected with Through Silicon Vias (TSV)

Increased integration, means

- Increase Cu resistivity (smaller grains)
- Increase heat (higher current density)
- Larger Thermomechanical stress

NPL will develop tools to measure energy dissipation with better lateral resolution, better accuracy and develop new procedures for conformity assessment at the wafer level



European Industrial Roadmap 2014



Heating in current-carrying nanostructures

Question 1: how is a local temperature defined and calculated?

Question 2: how is a heat defined?

Surface temperature measurement

- 'Surface' is an infinitely thin boundary between two objects
- No 'system' into which a thermometer can be immersed
- Does 'surface temperature' exist?
- What do you actually want to know?

Key equations

- All three partial errors are proportional to heat flux that flows up the probe due to the temperature gradient from object to probe
- If the heat flux is reduced, the measurement error is reduced

‘surface loading’

$$\Delta T_1 = -\frac{R_T}{\lambda_b} F_m q_T$$

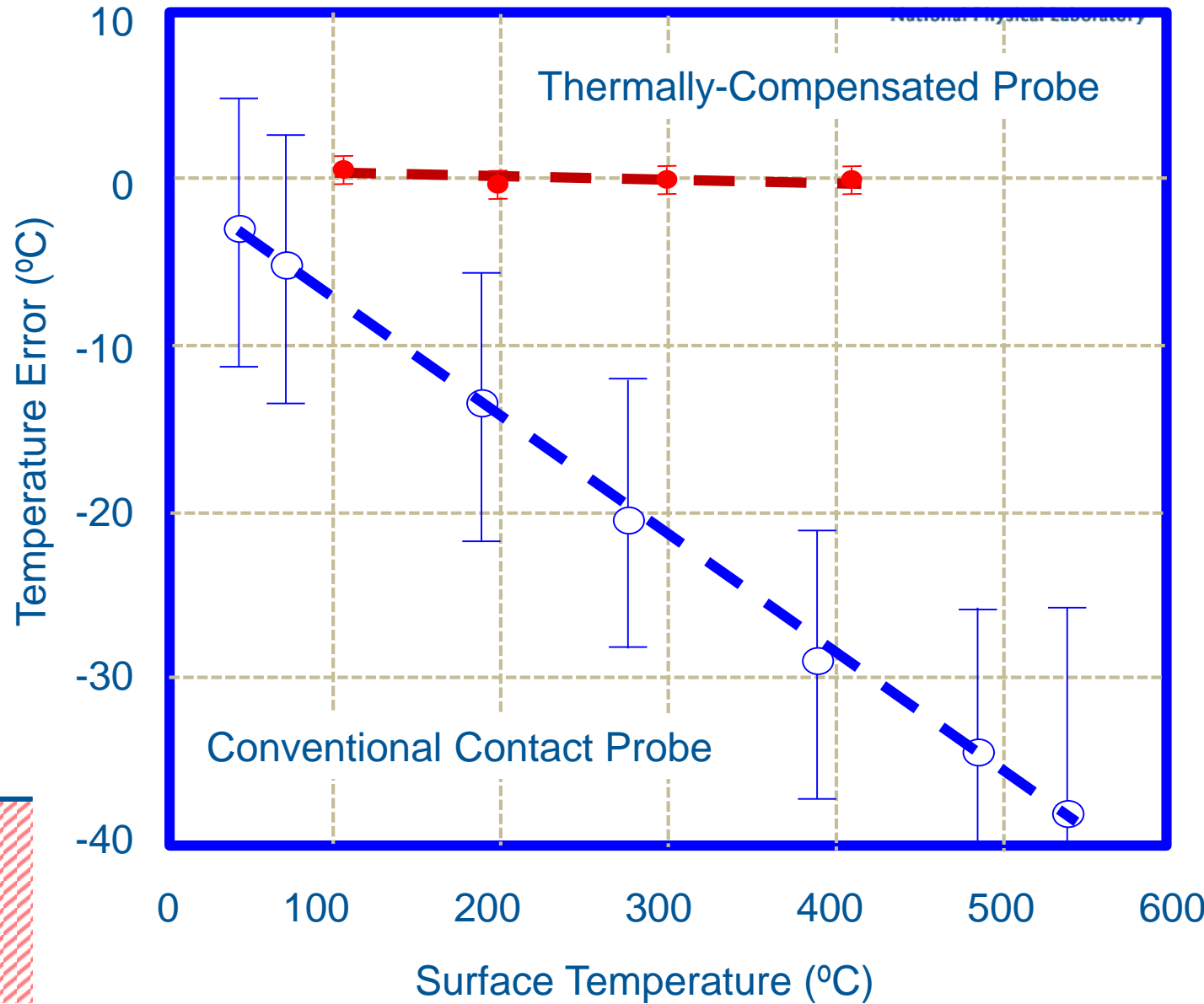
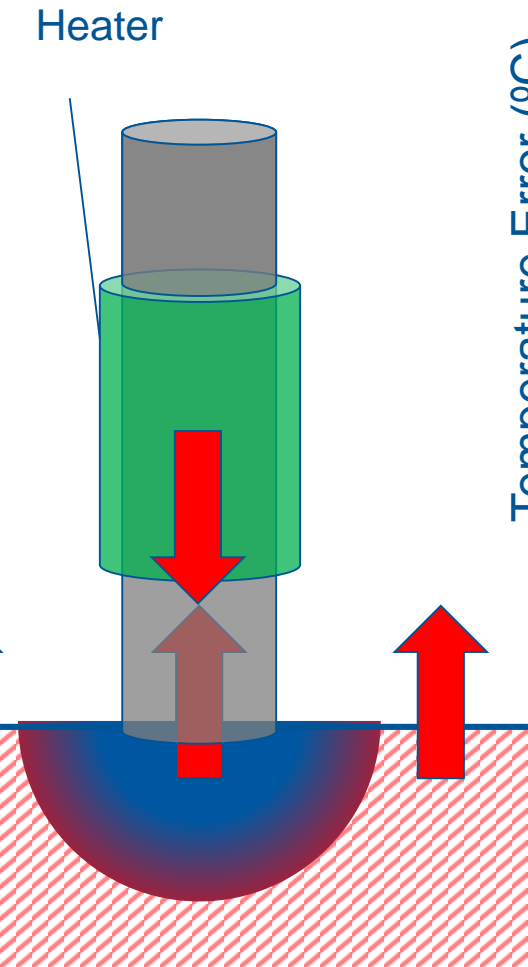
‘contact resistance’

$$\Delta T_2 = -W_C q_T$$

‘probe resistance’

$$\Delta T_3 = -\frac{L}{\lambda_T} q_T$$

Thermally-compensated probe



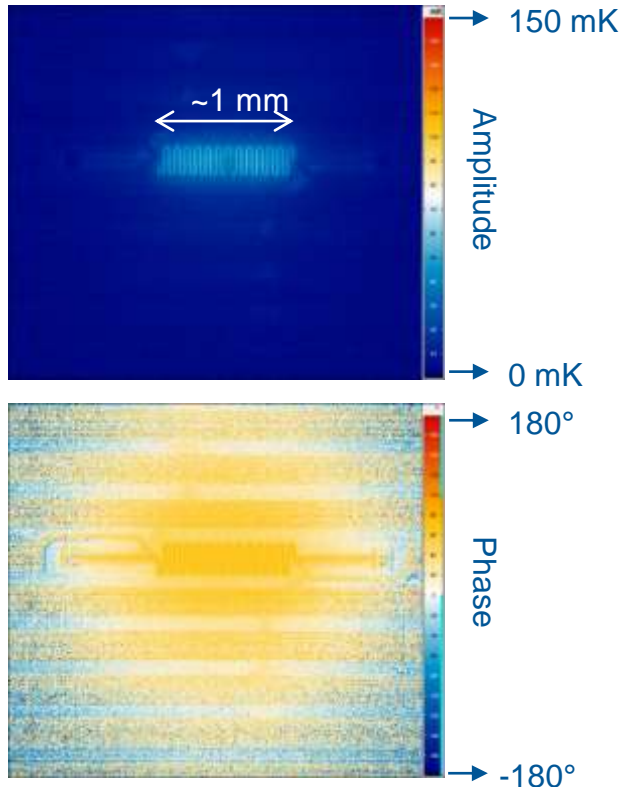
How do we measure temperature

- Resistance
- Thermocouples
- Coulomb blockade
- MEMS based resonator quality factor
- Fermi-level shift
- Thermo-chromic liquid crystals
- Infrared thermography
- Fluorescence
- Thermoluminescence
- Thermoreflectance
- Raman,

Near-field scanning optical or thermal microscopy

Lock-in infrared thermography

Measurement capabilities

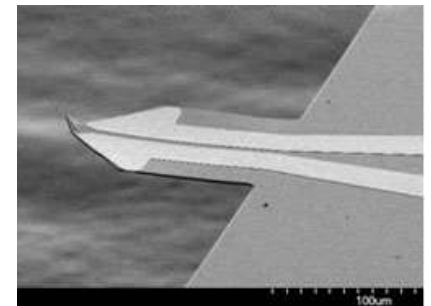


- Traceable temperature measurement
- mK change sensitivity
- Environmental enclosure with temperature control
- Capture rate up to 1.2 kHz

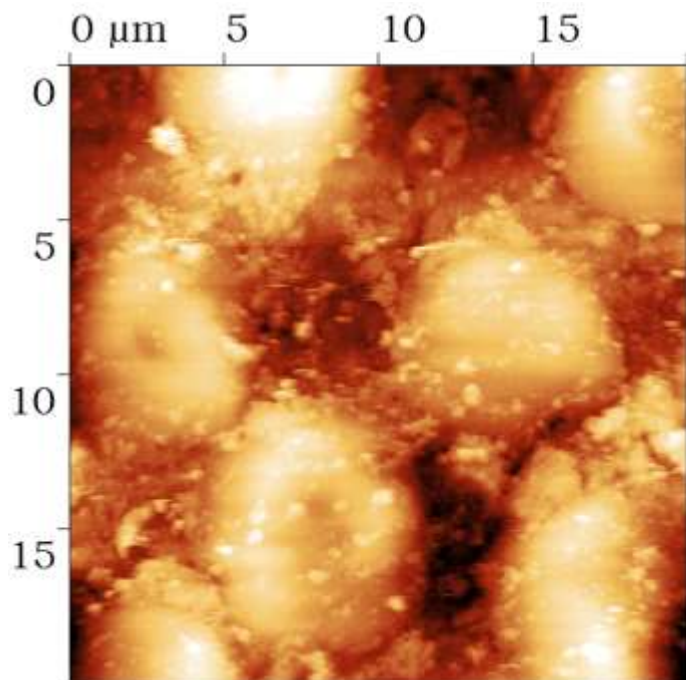


Scanning Thermal Microscopy

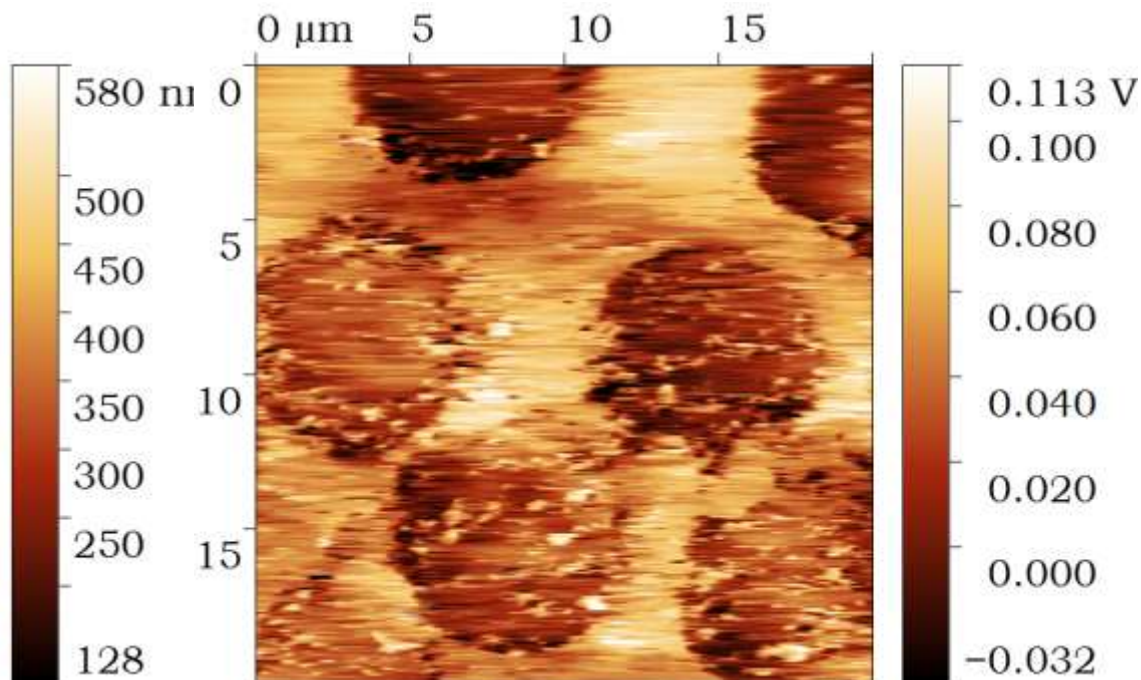
- A thermal sensor is mounted on a force sensitive cantilever and a feedback loops is used to maintain a constant tip-surface force while scanning
 - Cantilever deformation – bimorph effect
 - Thermal Drift and convection too slow to be a problem
- Thermal conduction = Thermal Transfer of heat through a material not involving mass transfer or emission of electromagnetic radiation
- Thermal conduction between the tip and the sample, change the temperature of the sensor



SThM Standard: carbon fibres in polymer matrix



Topography



Tip Voltage

Limitations

- So far mostly “limited” to materials with low thermal conductivity $0.1 < \kappa < 1 \text{ W/(m K)}$
 - Good for some polymers and thermoelectric !
 - Too low for semiconductors and metals, need to extend the range

- Heat transfer between the tip and the sample, change the temperature of the sensor

- Need to predict thermal resistance of nanosized contact

Transport regime in STM

Estimation of tip-sample contact radius

Derjaguin, Muller, Toporov approximation

Derjaguin et al J. Colloid Interface Sci. 53, 314 (1975)

$$\left(\frac{3 r_{tip} (F + 2\pi W r_{tip})}{4E^*}\right)^{1/3} = a_{DMT}$$

R_{tip} is measured by SEM,

W the work of adhesion extracted from force-curve,

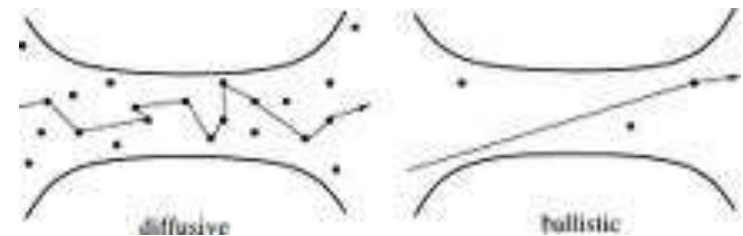
F the contact force is directly measured

E^* the effective Poisson's ratio can be estimated using nano-indentation

For Bi_2Te_3 the contact radius is approximately:

$a_{DMT} = 5 \text{ nm}$ for platinum tip

$a_{DMT} = 15 \text{ nm}$ for diamond tip



Transition between ballistic (Sharvin) and diffusive (Maxwell) regime

Nikolic and Allen, PRB 60,3963 (1999)

Conclusions

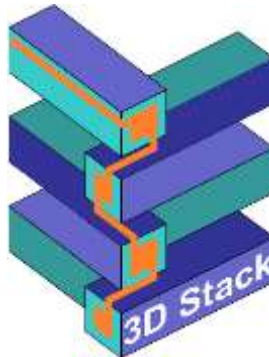
While the materials maybe different, the measurands : heat dissipated in current carrying semiconductor is identical to IT and consumer electronics sector

**Take home message:
Energy transport at the nanoscale is not obviously defined
Anisotropy matters**



Department
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Innovation & Skills

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