

# Mark scheme

## Section A

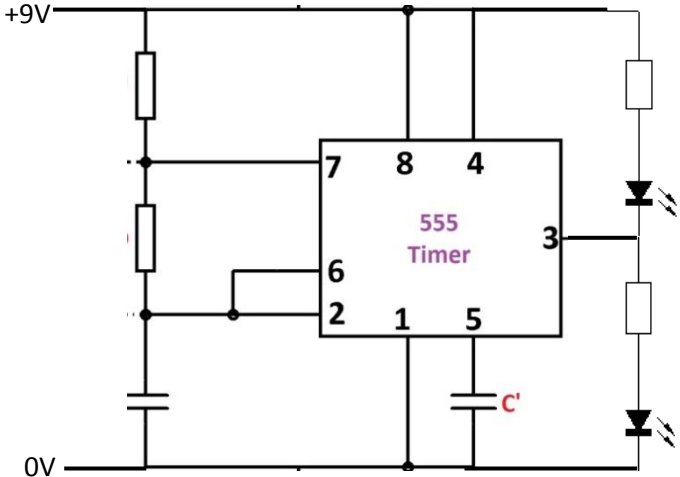
Question	Part	Sub part	Marking guidance	Marks
1	a		<p>Design requirements – <i>up to 4 marks</i>            1 mark for each requirement.            2 case and 2 circuit requirements asked for.</p> <p>Case: reference to a material, colour, dimension, safety, hygiene, weight, number of shapes, theme or similar. <i>1 mark each</i>  <i>e.g. the case needs to be lightweight..</i></p> <p>Circuit: reference to output component, input, battery access, circuit board, function of circuit, small or compact circuit ( not just ‘small’ ) or similar. <i>1 mark each</i>  <i>e.g. the circuit output needs to have bright LEDs.</i></p> <p>Do not credit repetition of the requirements given in the brief</p>	Total (4 marks)
	b		<p>Mark both designs holistically, giving credit for best features.</p> <p>7-8 marks – 2 creative designs that meet all the stated criteria and the ideas are well-communicated.</p> <p>5-6 marks – 2 ideas with less evidence of creativity, but still meeting the criteria, and good communication.</p> <p>3-4 marks – 1 idea that is clearly communicated, or 2 ideas with little creativity and detail.</p> <p>0-2 marks – a very basic attempt with poor clarity of communication.</p> <p>Evidence of creativity could include, for example, bright colours, loud sound output, light output pattern, interesting sequence of LEDs, shape of the case.</p>	Total (8 marks)
	c		<p>Case development - <i>up to 10 marks.</i></p> <p>Case construction details – <i>up to 2 marks</i>  <i>1 for each ref. to a method e.g. vacuum forming, injection moulding, gluing, joints, dimensions, battery access</i></p> <p>materials – <i>up to 2 marks</i>  <i>1 mark to ref. to a generic material e.g wood, metal, plastic.</i>  <i>2 marks for a specific material e.g. HIPS, polystyrene, acrylic, ABS, or similar suitable material.</i></p> <p>Switch/sensor details – <i>up to 3 marks</i>            1 mark for naming input component.            1 mark for indication of how it is triggered.            1 mark for suggesting how input component is fitted.</p> <p>Quality of communication of case development – <i>up to 3 marks</i>            0 marks for unclear development of how the case is made.</p>	Total (10 marks)

			<p>1 mark for basic sketch with little or no relevant annotation of how the case is made.</p> <p>2 marks for a clear sketch and some detailed annotation of case construction.</p> <p>3 marks for a coherent sketch, clearly communicated with good detail in annotations. A full and comprehensive design showing development from the ideas stage.</p>	
	<b>d</b>	<b>i</b>	<p>Reference to the use of a microcontroller, programmed to create a more complex and interesting output <i>-up to 2 marks</i></p> <p>1 mark for reference to programming.</p> <p>1 mark for creating a more complex output.</p> <p>e.g.  Several LEDs flashing in a sequence  Sounder playing a tune  Sounder playing 'sound effects'  Vibrations</p>	Total (2 marks)
	<b>d</b>	<b>ii</b>	<p>1 mark for naming "Process".</p> <p><i>1 mark</i> for each specific component named</p> <p>Input components could include PTM switch, reed switch, LDR, or other suitable response.</p> <p>Output components could include LED, bulb or lamp, buzzer, piezo transducer, sounder, bell, or similar.</p>	Total (5 marks)
	<b>e</b>		<p><i>Up to 4 marks</i> for circuit diagram and notes.</p> <p><i>1 mark</i> for a basic sketch showing some symbols for microcontroller <i>or</i> output components. No notes worthy of credit.</p> <p><i>2 marks</i> for a circuit diagram showing a microcontroller and output component(s), where parts of the circuit are correctly connected.</p> <p><i>3 marks</i> for a coherent circuit diagram with correct connections for outputs.</p> <p><i>4 marks</i> for circuit with both sound and light outputs connected to the outputs of a microcontroller.</p>	Total (4 marks)
	<b>f</b>		<p>Explanation of features such as:</p> <ul style="list-style-type: none"> <li>• Battery access</li> <li>• Easy to clean</li> <li>• Removable parts for easy repair</li> </ul> <p>Award <i>2 marks</i> for each justified point made.  Award <i>1 mark</i> for a simple, unjustified point.</p>	Total (2 marks)

## Section B

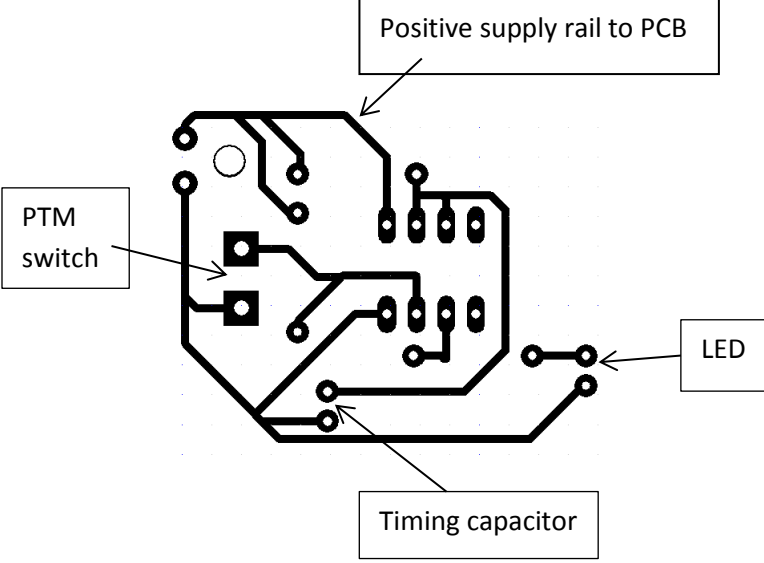
<b>2</b>	<b>a</b>	<b>i</b>	Any relevant, suitable product named	Total (1 marks)
		<b>ii</b>	<p><i>Up to 3 marks</i></p> <p><i>1 mark each</i> for reference to</p> <ul style="list-style-type: none"> <li>• Can be reprogrammed</li> <li>• Can be used in place of multiple ICs</li> <li>• Can result in smaller PCB</li> <li>• Can be programmed to perform different functions in different products</li> </ul> <p>Or similar suitable response.</p>	Total (3 marks)
	<b>b</b>	<b>i</b>	1 mark for an answer in the range 3 to 5 volts.	Total (1 mark)
		<b>ii</b>	1 mark for an answer naming a voltage regulator	Total (1 mark)
	<b>c</b>		<p><i>Up to seven marks</i></p> <p>1 for recognition of an input (decision box, if-then)</p> <p>1 for outputs on</p> <p>1 for wait 0.25 seconds</p> <p>1 for outputs off</p> <p>1 for wait 0.25 seconds</p> <p>1 for repeating the sequence twice more</p> <p>1 for loop back to start</p> <p>Basic or other program systems acceptable (simple re-writes of the question, giving no evidence of a programming system – no marks)</p>	Total (7 marks)

<b>3</b>	<b>a</b>	<b>i</b>	1 mark for each suitable description	Total (3 marks)
		<b>ii</b>	Astable – a system with no stable states; can be used to generate a pulse	
		<b>iii</b>	Monostable - a system with one stable state which, when triggered can be used to produce a time delay	
			Bistable – a system with two stable states that needs a trigger to switch between each state	Total (3 marks)
	<b>b</b>		<p><i>1 mark for each correct answer</i></p> <p>IC: Integrated circuit</p> <p>DIL: Dual in line</p>	Total (2 marks)

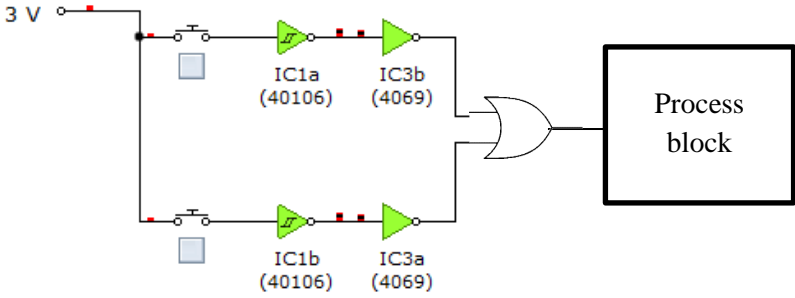
	<b>c</b>	<p><b>i</b> <i>Award marks as follows, up to a maximum of 6 marks</i></p> <p>1 mark for correct LED symbols  1 mark for correct LED orientation  1 mark for LED protective resistors  1 mark for connections that will cause the LEDs to flash alternately (one sinking and one sourcing pin 3 of the 555)  1 mark for positive supply rail  1 mark for 0 volt rail</p> 	Total (6 marks)
	<b>c</b>	<p><b>ii</b> <i>1 mark for correct answer</i></p> <p>Pulse</p>	Total (1 mark)
	<b>d</b>	<p><i>1 mark for each of the following correct points</i></p> <p>1 mark for drawing a pulse  1 mark for indicating a period of 1 second  1 mark for drawing having equal mark-space ratio  1 mark for indicating mark and space</p>	Total (4 marks)

4	<b>a</b>	<p><i>1 mark for each correct answer</i></p> <p>Answers relating to:  CAD  Advantages:  Quick and easy to modify  Files can be saved, stored and retrieved  Files can be emailed  Wide range of components available  Can see if circuit works without buying components</p> <p>Disadvantages:  Expensive to set up  Software may not have all components  Takes time to learn software  Hardware/software faults can cause work to be lost</p> <p>Breadboard  Advantages:  Uses real components  Gives indication of size of circuit</p>	Total (4 marks)
---	----------	---	--------------------

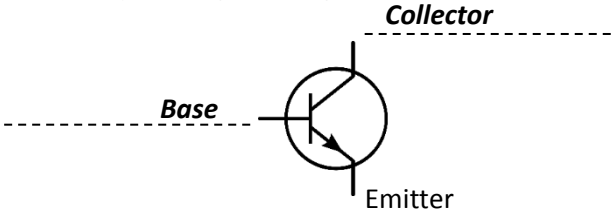
			<p>Components easily replaced</p> <p>Disadvantages:          Many components may need purchasing          Damage to components not always evident          Can be relatively slow to build a circuit          Can be difficult to fault find on a large, complex circuit</p> <p><i>Accept any suitable answers</i></p>	
	<b>b</b>		<p><i>Photo-etch method</i>  <i>1 mark awarded for each correct stage.</i></p> <ol style="list-style-type: none"> <li>1. Design the PCB and produce a mask (CAD or hand drawn)</li> <li>2. Expose photo etch board to ultra violet light</li> <li>3. Develop the image</li> <li>4. Etch the board in a bubble-etch tank</li> <li>5. Wash the etched board</li> <li>6. Clean and drill ready to be populated</li> </ol> <p><i>CNC method</i>  <i>1 mark awarded for any stages from:</i></p> <ol style="list-style-type: none"> <li>1. Design the using CAD</li> <li>2. Convert to PCB / autoroute</li> <li>3. Create CNC CAM file</li> <li>4. Clamp copper clad board in position</li> <li>5. Set Z position</li> <li>6. Route using CNC router</li> <li>7. Drill holes using CNC drill</li> </ol>	Total (6 marks)
	<b>c</b>	<b>i</b>	<p><i>2 marks available for each QC check</i>  <i>1 mark for simple answer</i>  <i>2 marks for explanation</i></p> <p>Suggested answers:          Continuity of tracks: visual check or using a multimeter          Size of holes so that pins/wires will fit          No tracks/pads missing          Tracks and pads have been cleaned to help create solder joints / prevent dry joints</p>	Total (4 marks)

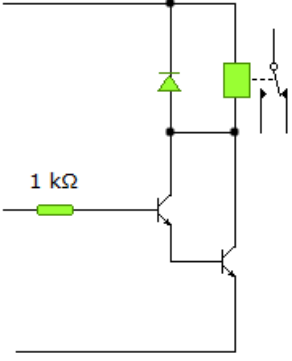
	<p><b>c</b></p>	<p><b>ii</b></p>	<p><i>Award 1 mark for each correct addition</i>  <i>Position on PCB can vary but must be correct relative to other components and circuit diagram</i></p>  <p>The diagram shows a PCB layout with several components and their connections. A 'Positive supply rail to PCB' is indicated at the top. A 'PTM switch' is on the left. A 'Timing capacitor' is at the bottom. An 'LED' is on the right. Arrows point from text boxes to each of these components.</p>	<p>Total (4 marks)</p>
--	-----------------	------------------	---	----------------------------

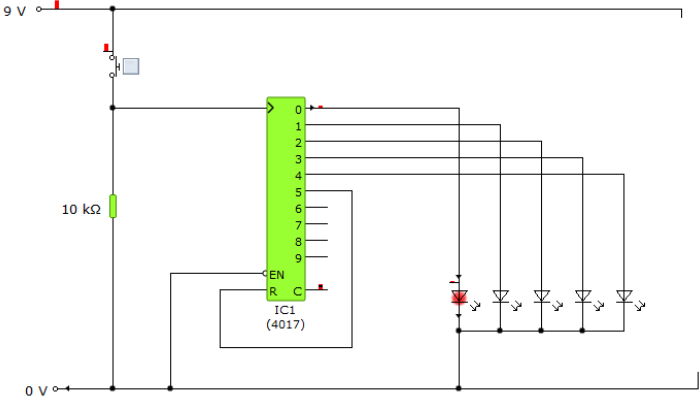
<p><b>5</b></p>	<p><b>a</b></p>	<p><b>i</b></p>	<p><i>1 mark for correctly naming the type of gate</i>                   AND gate</p>	<p>Total (1 marks)</p>															
	<p><b>a</b></p>	<p><b>ii</b></p>	<p><i>1 mark for each correct output; 1 mark for the correct input combination</i></p> <table border="1" data-bbox="435 1171 1166 1357"> <thead> <tr> <th>Input B</th> <th>Input A</th> <th>Output</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> </tr> </tbody> </table>	Input B	Input A	Output	0	0	0	0	1	0	1	0	0	1	1	1	<p>Total (4 marks)</p>
Input B	Input A	Output																	
0	0	0																	
0	1	0																	
1	0	0																	
1	1	1																	

	<p><b>b</b></p>	<p><b>i</b></p>	<p>OR gate drawn in correct position</p>  <p>The diagram shows a 3V supply connected to two inverters (IC1a and IC1b, both 40106) and two buffers (IC3a and IC3b, both 4069). The outputs of the inverters are connected to the inputs of the buffers. The outputs of the buffers are connected to an OR gate symbol, which is then connected to a 'Process block'.</p>	<p>Total (1 marks)</p>
	<p><b>b</b></p>	<p><b>ii</b></p>	<p><i>1 mark for correctly named OR gate</i></p>	<p>Total (1 marks)</p>

<b>6</b>	<b>a</b>	<p><i>1 mark for each point made, up to 3 marks</i></p> <p>Suggested answers:          Identical products          Very accurate / high tolerance          Very little waste          Waste material can be recycled          Manufacture can be automated, allowing for continuous production          Possible to make in a range of colours</p> <p>Accept any suitable answer</p>	Total (3marks)
	<b>b</b>	<p><i>1 mark for each point made, up to 3 marks</i></p> <p>Relatively inexpensive so can be done in schools          Easy to shape formers using hand tools          Can be made using different colours          Former can be reused          Easy to cut and finish materials          Equipment can be operated by student</p> <p>Accept any suitable answer</p>	Total (3 marks)
	<b>c</b>	<p><i>1 mark for appropriate features, up to 3 marks</i></p> <p>Features to include:          Draft angle on sides          Flat base          Rounded corners          Vent holes (counterbored)          Smooth surface</p>	Total (3 marks)

<b>7</b>	<b>a</b>	<p><b>i</b> <i>1 mark for correctly naming the arrangement</i></p> <p>Darlington pair</p>	Total (1 marks)
	<b>a</b>	<p><b>ii</b> <i>1 mark for correctly naming each leg</i></p> <div style="text-align: center;">  </div>	Total (2 marks)
	<b>b</b>	<p><i>1 mark for formula</i></p> $V_{\text{out}} = \frac{R_2}{R_1 + R_2} \times V_s$ <p><i>1 mark for substitution</i></p> $V_{\text{out}} = \frac{3700}{6300 + 3700} \times 12$ <p><i>1 mark for correct answer</i></p> <p>4.44 volts</p>	Total (3 marks)

	<b>c</b>	<b>i</b>	<p>1 mark for diode connected between Darlington collector connection and positive supply rail 1 mark for correct orientation</p> 	Total (2 marks)
	<b>c</b>	<b>ii</b>	1 mark for correctly naming diode	Total (1 marks)

<b>8</b>	<b>a</b>	<p>1 mark for a resistor connected between the switch and 0V rail 1 mark for connecting switch to +V rail and connecting switch to clock input of IC 1 mark for connecting IC output pin 5 to reset pin</p> 	Total (3 marks)
	<b>b</b>	<p><i>1 mark for simple answer</i> <i>2 marks for explanation</i></p> <p>When a mechanical switch is pressed the switch contacts may bounce against each other, turning the switch on and off rapidly, creating several unwanted input pulses.</p>	Total (2 marks)

<b>9</b>		<p>QWC question Looking for examples of the impact of sustainability and sustainable design that apply during the life cycle of a product.</p> <p>Discussion could include: Renewable materials Use of renewable energy in manufacture, processing, distribution, etc. Maintenance Recycling Planned obsolescence Disposal Pollution Environmental impact</p>	Total (8 marks)
----------	--	---	--------------------



		<p>Social and cultural issues</p> <p>Consider the technical content and quality of communication.  Marks awarded as follows:  0 marks – no answer worthy of credit.  1-2 marks  Limited coverage. Just one point discussed. Many spelling and punctuation mistakes. Limited use of technical vocabulary.  3-4 marks  Discussion of advantages and disadvantages of two products. Some spelling, punctuation or grammar errors. Poor structure of answer, and repetition made.  5-6 marks  Good coverage and a well-structured response. Advantages and disadvantages for at least two products discussed using specific terms and vocabulary. There may be one or two spelling or punctuation mistakes, or minor grammar error.  7-8 marks  Excellent coverage and depth of answer, and a well-structured response. Several products discussed using technical terms in good detail. Excellent spelling, grammar and punctuation. Avoidance of repetition.</p>	
--	--	--	--