NXP GaN Transistor Pairing with Controller & Switch

November 2016

Sizing is based on individual GaN Peak Power Output and the current drawn determines the size of the MOS Switch. Multiple GaN devices per single MOS Switch is allowed if total current do not exceed the maximum. The list assumes that devices are operating in CW.

Model Number	Freq min (GHz)	Freq max (GHz)	Gain (dB)	Pout (W)	Vds (V)	Typ Eff (%)	Ids (A)	MOS size (A) min	Controller	Switch (Pulsed or CW)	Eval Board, Drop-In (Pulsed or CW)	Eval Board, Removable (Pulsed Only)
A2G22S160-01S	1800	2200	19	32	48	35%	1.90	3.8	100X,200X	392P,395C,332P,335C	610E,620E	
A2G22S251-01S	1805	2200	17	48	48	35%	2.86	5.7	100X,200X	392P,395C,332P,335C	610E,620E	
A2G26H281-04S	2496	2690	14	50	48	50%	2.08	4.2	100X,200X	392P,395C,332P,335C	610E,620E	
A2G35S160-01S	3400	3600	15	32	48	34%	1.96	3.9	100X,200X	392P,395C,332P,335C	610E,620E	
A2G35S200-01S	3400	3600	16	40	48	33%	2.53	5.1	100X,200X	392P,395C,332P,335C	610E,620E	
MMRF5014HR5	0.00	2.70	12	100	50	40%	5.00	10.0	100X,200X	332P,335C,362P,365C	630E,640E	732E,735E,762E,765E
MMRF5015NR5	0.00	2.70	12	100	50	40%	5.00	10.0	100X,200X	332P,335C,362P,365C	630E,640E	732E,735E,762E,765E

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