



# 2-Mbit (128K x 16) Static RAM

## Part Number: DPA71011DV3302A

The DPA71011DV3302A is a high-performance CMOS Static RAM organized as 128K words by 16 bits.

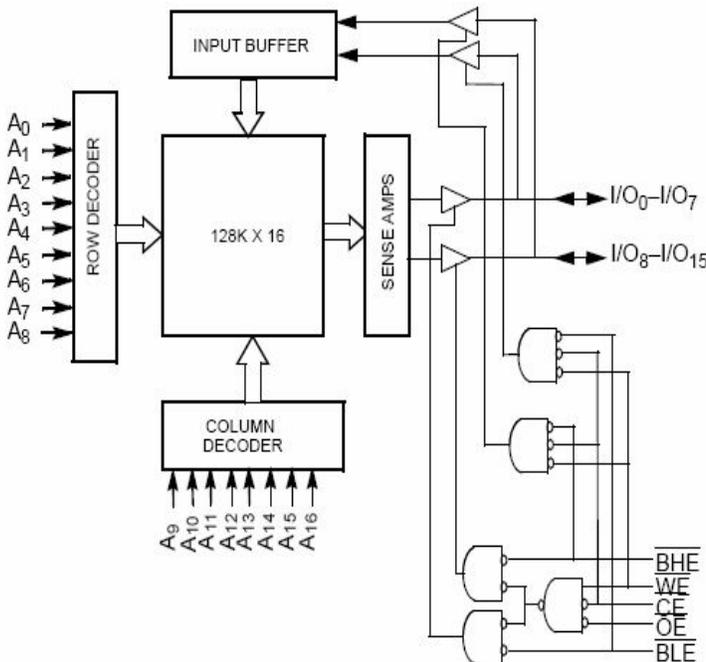
Writing to the device is accomplished by taking Chip Enable ( $\overline{CE}$ ) and Write Enable ( $\overline{WE}$ ) inputs LOW. If Byte Low Enable ( $\overline{BLE}$ ) is LOW, then data from I/O pins (I/O<sub>0</sub> through I/O<sub>7</sub>), is written into the location specified on the address pins (A<sub>0</sub> through A<sub>16</sub>). If Byte High Enable ( $\overline{BHE}$ ) is LOW, then data from I/O pins (I/O<sub>8</sub> through I/O<sub>15</sub>) is written into the location specified on the address pins (A<sub>0</sub> through A<sub>16</sub>).

Reading from the device is accomplished by taking Chip Enable ( $\overline{CE}$ ) and Output Enable ( $\overline{OE}$ ) LOW while forcing the Write Enable ( $\overline{WE}$ ) HIGH. If Byte Low Enable ( $\overline{BLE}$ ) is LOW, then data from the memory location specified by the address pins will appear on I/O<sub>0</sub> to I/O<sub>7</sub>. If Byte High Enable ( $\overline{BHE}$ ) is LOW, then data from memory will appear on I/O<sub>8</sub> to I/O<sub>15</sub>.

The input/output pins (I/O<sub>0</sub> through I/O<sub>15</sub>) are placed in a high-impedance state when the device is deselected ( $\overline{CE}$  HIGH), the outputs are disabled ( $\overline{OE}$  HIGH), the  $\overline{BHE}$  and  $\overline{BLE}$  are disabled ( $\overline{BHE}$ ,  $\overline{BLE}$  HIGH), or during a write operation

- **-55° to +125°C operating temperature**
- **High speed**
  - t<sub>AA</sub> = 12 ns
- **Low active power**
  - I<sub>CC</sub> = 90 mA @ 12 ns
- **Low CMOS standby power**
  - I<sub>SB2</sub> = 10 mA
- **Supply voltage**
  - 3.3 V dc
- **2.0 V data retention**
- **Automatic power-down when deselected**
- **Independent control of upper and lower bits**
- **Easy memory expansion with  $\overline{CE}$  and  $\overline{OE}$  features**
- **44-pin SO ceramic flatpack, same footprint as 44-pin TSOP II**
- **Drop-in replacement for Cypress CYC1011DV33**
- **Custom packaging is available**
- **This product uses Cypress CYC1011DV33 die and is tested to meet military and space operational environment requirements.**

### Logic Block Diagram



### Pin Configuration

